

Electronic Acknowledgement Receipt

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Application Number:	10574030
International Application Number:	
Confirmation Number:	8030
Title of Invention:	Two-transistor memory cell and method for manufacturing
First Named Inventor/Applicant Name:	Robertus Theodorus Van Schaijk
Customer Number:	65913
Filer:	Peter Zawilski
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		NL031167US1_RestrictionResponseTraverse_2009-OCT-06.pdf	360442 dd9b1797dca4f5d6709fd097eff4cabbabe27a9	yes	9

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Response to Election / Restriction Filed	1	1
	Applicant Arguments/Remarks Made in an Amendment	2	9
Warnings:			
Information:			
Total Files Size (in bytes):		360442	
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